CLAIMS

1. A time sectionalized demodulator for pulse code modulated signal, comprising:

a carrier counter for counting the number of pulses of an incoming signal within a time slot;

a digital comparator for comparing said number of pulses with a preset number N , yielding a high voltage level when the number of pulses equals or exceeds N and a digital "0" when the number of pulses is less than N;

a time slot generator for generating time slots shorter than time window, and for sectionalizing said incoming signal; and

a latch fed by said comparator and enabled by said time slot generator to output voltage level at the end of each time slot.

2. A time sectionalized demodulator as described in claim 1, further comprising a decoder for converting the voltage output of said latch into digital signal.

4. A time sectionalized demodulator as described in claim 1, wherein said preset number is at least equal to one.

5. A time sectionalized demodulator as described in claim 1, wherein the number of time slots per time window is at least one.

6. A time sectionalized demodulator as described in claim 1, wherein said preset number is preset within said comparator.

7. A time sectionalized demodulator as described in claim 1, wherein said preset number is programmed by a register.